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Application No. Filing Date

First Named Inventor: K. NAGATANI

Group Art Unit **Examiner Name**

Attorney Docket No.: FUSA 19.236

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 1

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	U.S. Patent Document	Kind Code if known ²	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear
A		4,700,151		Nagata	10/13/87	
#		5,903,611		Schnabl et al.	05/11/99	
B		5,164,678		Puri et al.	_11/17/92	

FOREIGN DOCUMENTS						
Examiner Initials	Cite No.	Foreign Patent Document Office ³ Number ⁴ Kind Code ³ (if known)	Country	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear
AB3		091164741	JP	Sony Corp.	05/02/97	
1		10136048	JP	Hitachi Denshi Ltd.	05/22/98	1
1		62139425	JP	Nec Corp	06/23/87	
		63121326	JР	Nec Corp.	05/25/88	<u> </u>
	 	58084558	JР	Nec Corp.	05/20/83	<u> </u>
B		10145146	JP	Matsushita Electric Ind. Co Ltd.	05/29/98	

Other Prior Art-Non Patent Literature Documents

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), data, page(s), volume-issue number(s), publisher, country, where published, source.	Applicant check her if English language . translation attached
B		NAGATANI, ET AL. Simulated Tolerance of Loop Parameters of Cartesian Feedback Amplifier for 11/4 Shifted QPSK Transmitter June 26, 1992, pgs 25-30	
*		IEEE Transactions on Signal Processing June 1998 Volume 6 pgs. 1726-1730	

Examiner Signature A. Behulu	Date Considered	8·18·04
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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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